Credo Technology (Hong Kong) Limited
Credo Semiconductor is founded in 2008 and profitable since 2012. Revenue double yearly from 2014. Credo has been award top50 startup watch list in last two years on US EEtimes. Credo focus on single lane 28G/56G/112G SerDes interconnect technology for mega data centers. Our design can be found in TSMC 65nm/40nm/28nm and 16nm process. We have ~100 technical staffs in US, Shanghai and Hong Kong. With the high demand to our products, we plan to double our R&D head counts. Visit [www.credosemi.com](http://www.credosemi.com) for more information.

We invite you to be part of the team and bring the company to success.

**Analog Design Engineer**

**Duties:**

- Participate in a team to design and layout of high-speed ICs for optical fiber/wireline communications
- Design, verification, simulation and optimization for amplifier, ADC/ DAC, bandgap, analog buffer, LDO, charge pump, voltage reference, PLL, oscillator, SerDes, etc.
- Create support documents
- Travel may be required if necessary.

**Requirements:**

- Bachelor degree or above in Electronic Engineering
- Knowledge in analog and mixed-signal circuit design
- Familiar with simulation tools such as SPICE, SPECTRE
- Experienced in high-speed circuit design is a plus
- Knowledge in CMOS IC physical layout design and able to use various EDA tools
- Good understanding of semiconductor theory and structures of devices
- Team spirit and strong communication skills
- Self-motivated with good working attitude
- Candidate with more solid experiences will be considered for a senior position
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<thead>
<tr>
<th>Email:</th>
<th><a href="mailto:jobs@credosemi.com">jobs@credosemi.com</a></th>
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<tbody>
<tr>
<td>Post:</td>
<td>Suite 103B, 1/F Enterprise Place (Bldg 9), #5 Science Park West Ave, Hong Kong Science Park, Shatin, N.T. Hong Kong</td>
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Only short-listed candidates will be notified. Personal data provided by applicants will be used for recruitment purposes only.