**Credo Technology (Hong Kong) Limited**  
Credo Semiconductor is founded in 2008 and profitable since 2012. Revenue double yearly from 2014. Credo has been award top50 startup watch list in last two years on US EEtimes. Credo focus on single lane 28G/56G/112G SerDes interconnect technology for mega data centers. Our design can be found in TSMC 65nm/40nm/28nm and 16nm process. We have ~100 technical staffs in US, Shanghai and Hong Kong. With the high demand to our products, we plan to double our R&D head counts. Visit [www.credosemi.com](http://www.credosemi.com) for more information.

We invite you to be part of the team and bring the company to success.

**ASIC Design Engineer**

**Duties:**
- Candidate will be part of core design team carrying the task to deliver the next generation super high-speed data IC for home networking.
- Candidate is responsible for SOC integration and verification.
- Travel may be required if necessary.

**Requirements:**
- Bachelor degree or above in Electronic Engineering
- Good understanding of SoC, architectures, methodologies and related tools
- Good understanding of microprocessors, and computer system architecture
- Good understanding of IPs, integration and verification
- Good programming in Perl, TCL and Shell programming
- Familiar with Ethernet, USB, MCU(ARM core), I2C, MDIO, usage and protocols
- Team spirit and strong communication skills
- Enjoy challenging work and a Self-motivated good team player
- Candidate with more solid experiences will be considered for a senior position
<table>
<thead>
<tr>
<th>Email</th>
<th><a href="mailto:jobs@credosemi.com">jobs@credosemi.com</a></th>
</tr>
</thead>
<tbody>
<tr>
<td>Post</td>
<td>Suite 103B, 1/F Enterprise Place (Bldg 9), #5 Science Park West Ave, Hong Kong Science Park, Shatin, N.T. Hong Kong</td>
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</tbody>
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Only short-listed candidates will be notified. Personal data provided by applicants will be used for recruitment purposes only.