About Research Dept. of Hisilicon and job description in low-power area:

Research Dept. of Hisilicon aims at innovative research and imminent technique development in semiconductors and related design. Low-power design is one of the directions that are guiding contemporary and future work in electronics.

Job responsibilities for low-power direction:

Research in ultra low-power (ULP) design and RF harvesting, digital/analog circuit design, patent applications and paper publications (top international conference/journal, e.g., ISSCC/JSSCC), breakthrough in ULP area and as well applications in internet of things (IoT).

Job requests:

1. Ph.D. degree in microelectronics, electronics and information engineering, and related.
2. Experiences in tape-out (test chip/demo/chip-set).
3. Published paper at top international conference/journal.
4. Good at innovative design and practicing.

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