Position: Design Engineer

Location: Hong Kong

Job Responsibilities:

- Perform design implementation from RTL to GDS which includes RTL synthesis, Timing constraints creation, Static Timing Analysis, SCAN insertion and ATPG, Chip floor plan, Timing Driven Placement & Route, and Power Analysis.
- Improve efficiency and reliability of backend flow

Job Requirements:

- Postgraduate degree or above in Electronic Engineering or Computer Engineering or equivalent.
- At least 2 years relevant experience
- Familiarities / knowledge in display driver IC development or low power design development would be considered as an advantage

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by one of the following channels. Please quote our reference number in your application.

1. Mail: Human Resources Department, Solomon Systech Limited, 6/F, No. 3 Science Park East Avenue, Hong Kong Science Park, Shatin, N.T., or
2. E-mail: recruit@solomon-systech.com, or
3. Upload the resume directly by clicking the apply button on each job opening

We regret that only short-listed candidates will be notified.