**Job Title: Algorithm/DSP Software Engineer, Hong Kong**

In May 2008, HiSilicon (a wholly owned semiconductor subsidiary of Huawei) set up an R&D center in Hong Kong, focusing on communication chipset development. Today, we are expanding our R&D work to next generation products that would be used in a wide range of Huawei products. We are looking for enthusiastic and high caliber engineers at all levels to work with our team in Shenzhen and Hong Kong, to support the development from system and architectural design, modeling, algorithm derivation, DSP programming to system testing and verification. Travel between the 2 locations is required.

The Algorithm/DSP Software Engineers will be responsible or take part in defining the algorithm/DSP SW of different major functional units of a communication system, focusing on the physical and MAC layer. The individual shall be responsible for or contribute to the various phase of the development work, including but not limited to algorithm development, feasibility studies, performance modeling and analysis, MIPs analysis, DSP coding and testing. The individual shall work closely with the ASIC engineers to deliver the complete low cost, low power, and high performance solution for wireline networking application.

**Responsibilities**

✓ Develop communication algorithms and carry through implementation.
✓ Work on embedded DSP software in leading-edge communication systems and products.
✓ Generate design and implementation documentation.
✓ Perform system level integration, board bring-up and performance tuning.

**Desired Skills and Experience**

✓ Experience with the algorithm/DSP SW design/testing in one or more of the following disciplines
  ▪ Communication and signal processing theory, e.g. MIMO, diversity, estimation theory, filtering, FFT etc.
  ▪ Forward error correction Algorithm, e.g. LDPC, Turbo code, Reed Solomon, Trellis Coding, Viterbi etc.
  ▪ Noise Cancellation Algorithms/Scheme, e.g. Interleaving, Echo Cancellation etc.
  ▪ Probability and statistics, linear algebra, optimization theory
✓ Knowledge of ARM/Tensilica architecture, RTL design and verification is preferred.
✓ Must be a highly organized, detail-oriented self-starter, who works well independently, as well as in a team environment
✓ BS or higher degree in Electrical/Computer Engineering.
✓ Good verbal and written communication skills

Contact: eric.ng@huawei.com  ;  francis.ng@huawei.com  ;